

Fig. II-2-2-6 Resistivity distribution maps by Occams inversion in Azzouz

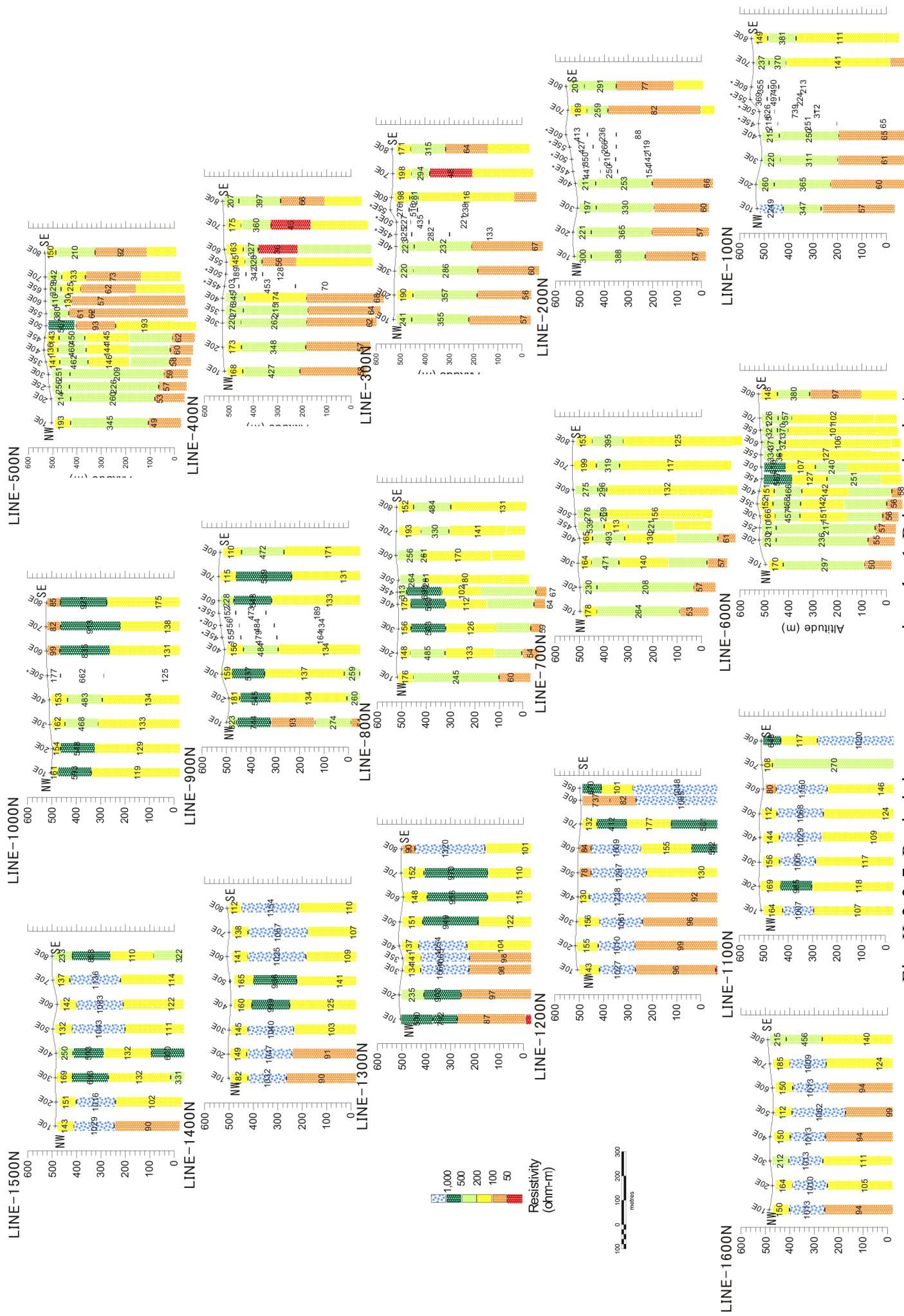


Fig. II-2-2-7 Resistivity structure sections by 1-D inversion in Azzouz

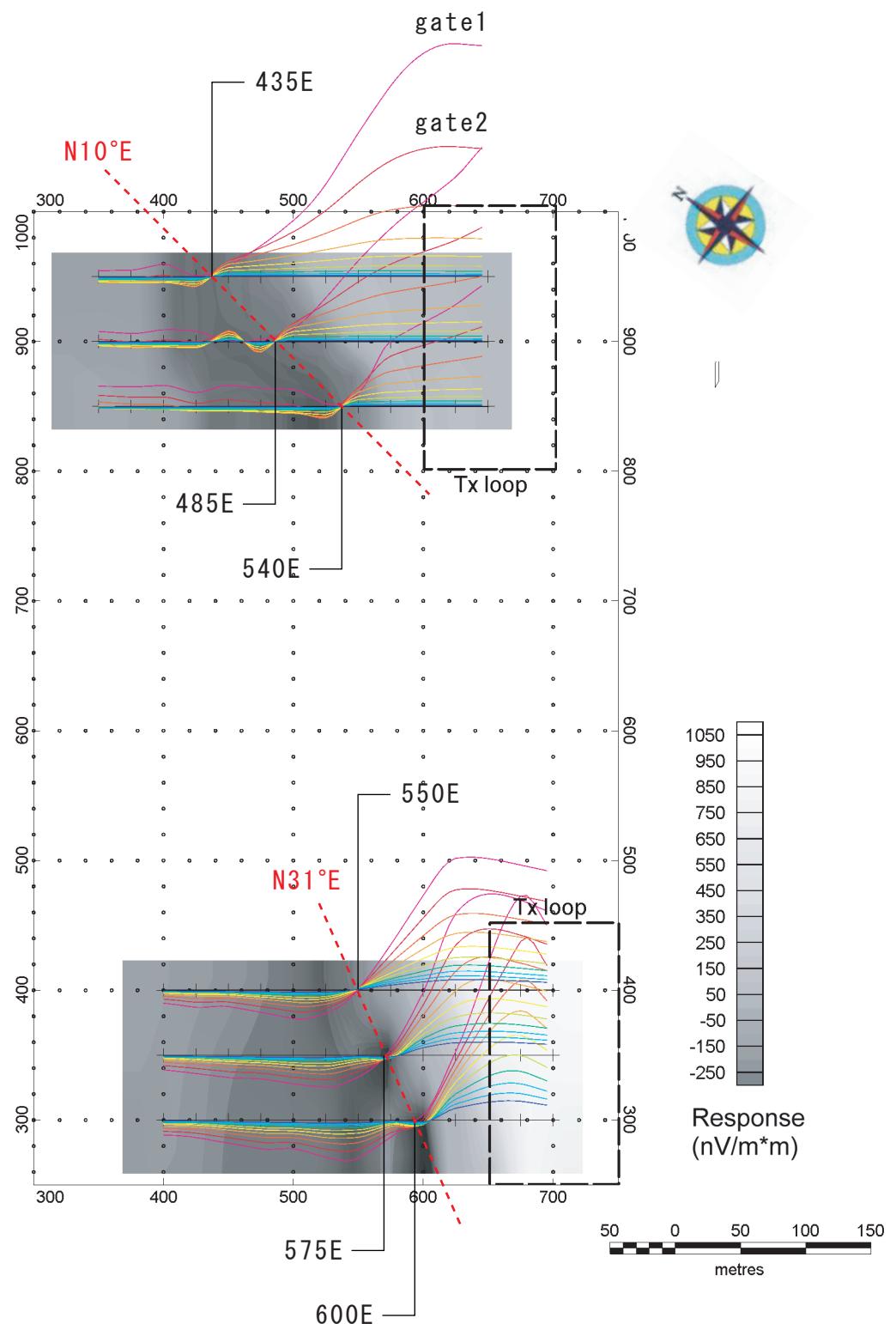
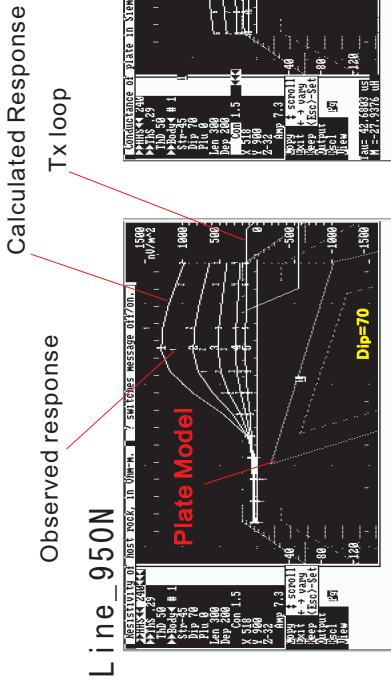


Fig. II-2-2-8 Normalized voltage profile by fixed loop configuration in Azzouz



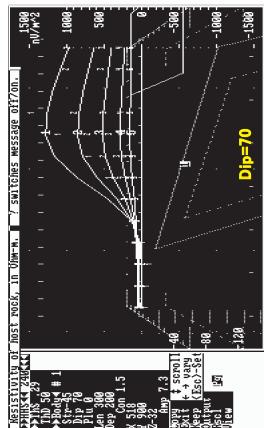
Gate1-5

Line_900N

Gate6-10

Gate11-15

Gate16-20



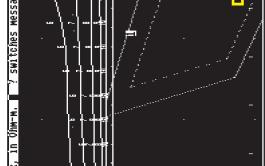
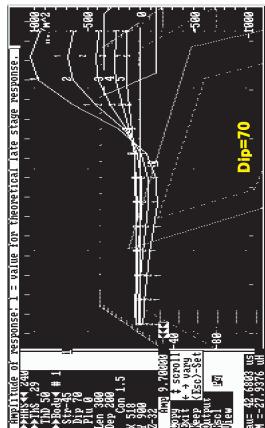
Gate1-5

Line_850N

Gate6-10

Gate11-15

Gate16-20



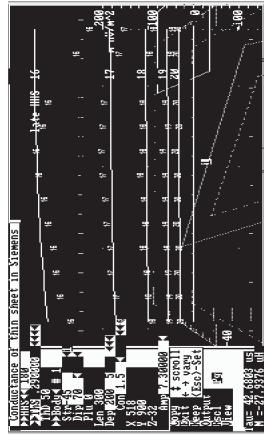
Gate1-5

Gate6-10

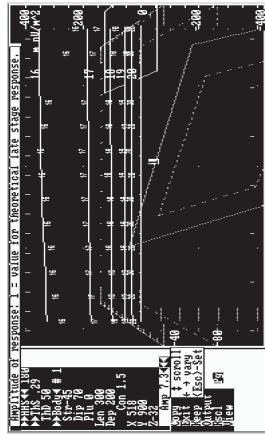
Gate11-15

Gate16-20

Fig. II 2-2-9(1) The result of simple plate analysis (1)



Gate16-20

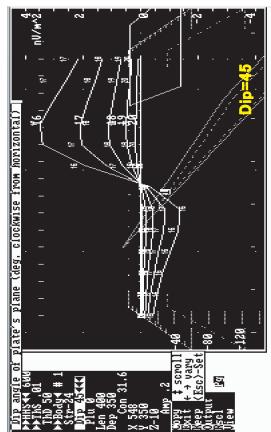


Gate16-20

Plate Model
Calculated Response

Line_400N

Tx loop



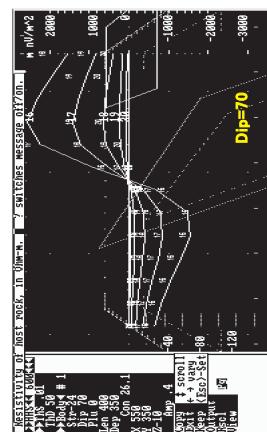
Gate16-20

Gate11-15

Gate6-10

Observed response
Gate1-5

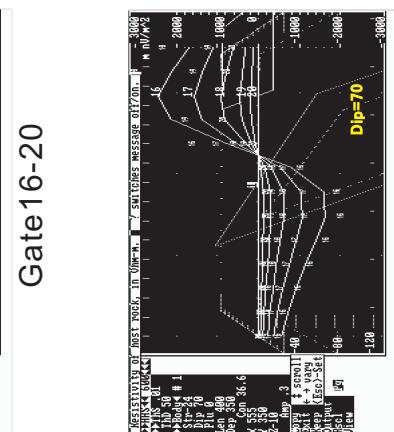
Line_350N
Line_300N



Gate16-20

Gate11-15

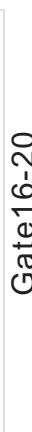
Gate6-10



Gate16-20

Gate11-15

Gate6-10



Gate16-20

Gate11-15

Gate16-20

Gate11-15

